EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1029	integrated with circuit.ti. and (thomas chiu).in.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/29 18:42
L2	43	layer with integrated with circuit. ti. and (thomas chiu).in.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/29 18:44
L3	63	(redistribut\$3 with layer with integrated with circuit).clm.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/29 18:46
L4	3	(redistribut\$3 with layer and integrated with circuit and polyimide with active).clm.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/29 18:46